Motivation	Considered Systems	Example	Summary & Outlook
0000	00000	0000000	00

# Constraint-based Approach for an Early Inspection of the Feasibility of Cyber Physical Systems

### Benny Höckner & Peter Sauer & Thilo Vörtler & Petra Hofstedt & Thomas Hinze

12. September 2013

Considered Systems

Example 00000000 Summary & Outlook

# Contents

1 Motivation

2 Considered Systems

#### 3 Example

4 Summary & Outlook

Considered Systems

Example 00000000 Summary & Outlook

# Motivation (I)

The importance of Embedded and Cyber Physical Systems (ES/CPS) is increasing more and more:

- digital camera
- cell / smart phones
- cars
- control system
- health care
- assisted living

 Image: A state of the stat

http://www.stw.nl/en/node/4709

...

М	ot	iv	а	t	io	n
0	0	0				

Considered Systems

Example 00000000 Summary & Outlook

# Motivation (II)

Designing them can be a tough task:

- lot of different hardware components with different configuration modes are connected to each other
- components heavily influence each other
- trade-off between rich functionality, lifetime and correct design

The following questions may arise:

- Is the system constructable regarding to the requirements?
- How much power will consume the system in use or how long will last the energy source?
- Is it possible to add further components and using which possible configurations?

I ...

Considered Systems

Example 00000000 Summary & Outlook

### Idea and Advantages

In the early stage, normally only partial information is available.

Idea

Constraints are well suited to deal with incomplete information. Thus we use them to model systems and to obtain valid systems.

Considered Systems

Example 00000000 Summary & Outlook

### Idea and Advantages

In the early stage, normally only partial information is available.

#### Idea

Constraints are well suited to deal with incomplete information. Thus we use them to model systems and to obtain valid systems.

#### Advantages

- System has not to be specified completely, but we nevertheless obtain some answers and also retrieve remaining configuration possibilities.
- Easy to model, since every requirement can be encoded by one or more constraints independent from other requirements.
- Reduce prototyping effort







Considered Systems

Example 00000000 Summary & Outlook

# Considered Systems

- For now, we focused on systems consisting of:
  - a single cpu/micro controller,
  - sensors/actuators,
  - buses
- Reason: Unknown whether the solver would be able to handle large systems.
- ... but the method is not limited to these systems.

Considered Systems

Example 00000000 Summary & Outlook

- CPU = (Vcc, f, I<sub>active</sub>, I<sub>inactive</sub>, PI<sub>1</sub>,..., PI<sub>m</sub>)
- *I* = (I<sub>low</sub>, I<sub>high</sub>)
- $\blacksquare Pl_i = \{Prot_1, \ldots, Prot_n\}$

Considered Systems

Example 00000000 Summary & Outlook

- CPU = (Vcc, f, l<sub>active</sub>, l<sub>inactive</sub>, Pl<sub>1</sub>, ..., Pl<sub>m</sub>)
- I = (Ilow, Ihigh)
- $\blacksquare Pl_i = \{Prot_1, \ldots, Prot_n\}$
- $BUS = (\{C_1^{BUS}, \ldots, C_p^{BUS}\}, overhead)$

Considered Systems

Example 00000000 Summary & Outlook

- CPU = (Vcc, f, l<sub>active</sub>, l<sub>inactive</sub>, Pl<sub>1</sub>, ..., Pl<sub>m</sub>)
- I = (Ilow, Ihigh)
- $\blacksquare Pl_i = \{Prot_1, \ldots, Prot_n\}$

• 
$$BUS = (\{C_1^{BUS}, \ldots, C_p^{BUS}\}, overhead)$$

• Sensor = 
$$(S, \{C_1^{Sensor}, \ldots, C_q^{Sensor}\}, size)$$

• 
$$C_i^{Sensor} = (VCC, f, I_{active}, I_{inactive}, \{Prot_1, \dots, Prot_{k1}\})$$

• 
$$S = \{ \texttt{StateId}_1, \dots, \texttt{StateId}_r \}$$

Considered Systems

Example 00000000 Summary & Outlook

- CPU = (Vcc, f, l<sub>active</sub>, l<sub>inactive</sub>, Pl<sub>1</sub>, ..., Pl<sub>m</sub>)
- / = (Ilow, Ihigh)
- $\blacksquare Pl_i = \{Prot_1, \ldots, Prot_n\}$

• 
$$BUS = (\{C_1^{BUS}, \ldots, C_p^{BUS}\}, overhead)$$

• Sensor = 
$$(S, \{C_1^{Sensor}, \ldots, C_q^{Sensor}\}, size)$$

• 
$$C_i^{Sensor} = (VCC, f, I_{active}, I_{inactive}, \{Prot_1, \dots, Prot_{k1}\})$$

• 
$$S = \{ \texttt{StateId}_1, \dots, \texttt{StateId}_r \}$$

• 
$$Module_i = (S, \{C_1^{Module}, \dots, C_s^{Module}\})$$

• 
$$C_i^{Module} = (\text{Vcc}, I_{active}, I_{inactive}, \{Prot_1, \dots, Prot_{k2}\})$$

Considered Systems

Example 00000000 Summary & Outlook

### Some constraints in such a system (1)

#### design requirements

- Modules can only be connected to interfaces, if they have at least one protocol in common.
- All sensors on the same bus have to use the same protocol, which is also supported by the interface.

Considered Systems

Example 00000000 Summary & Outlook

### Some constraints in such a system (1)

#### design requirements

- Modules can only be connected to interfaces, if they have at least one protocol in common.
- All sensors on the same bus have to use the same protocol, which is also supported by the interface.
- functional requirements
  - CPU should be able to process the data of all connected sensors.

$$t_{cpu_{active}} = (f_{sensor_1} * LOC_1 + \ldots + f_{sensor_n} * LOC_n)/f_{cpu}$$
  
 $0.0 \le t_{cpu_{active}} \le 1.0$ 

Mot	iv	at	io	n
000	0			

Considered Systems

Example 00000000 Summary & Outlook

### Some constraints in such a system (1)

### design requirements

- Modules can only be connected to interfaces, if they have at least one protocol in common.
- All sensors on the same bus have to use the same protocol, which is also supported by the interface.
- functional requirements
  - CPU should be able to process the data of all connected sensors.

$$t_{cpu_{active}} = (f_{sensor_1} * LOC_1 + \ldots + f_{sensor_n} * LOC_n) / f_{cpu}$$

$$0.0 \le t_{cpu_{active}} \le 1.0$$

bus should be able to transport all data of modules/sensors

$$f_{bus} \ge f_{sensor_1} * (size_1 + overhead_{bus}) + \ldots + f_{sensor_n} * (size_n + overhead_{bus})$$

Considered Systems

Example 00000000 Summary & Outlook

### Some constraints in such a system (II)

#### non-functional requirements

Overall power consumption should be less than or equal to c

$$\begin{aligned} P_{cpu} &= t_{cpu_{active}} * Vcc_{cpu} * I_{cpu_{active}} + (1 - t_{cpu_{active}}) * Vcc_{cpu} * I_{cpu_{inactive}} \\ P_{system} &= P_{cpu} + P_{bus} + \dots \\ P_{system} &\leq c \end{aligned}$$

Considered Systems

Example 00000000 Summary & Outlook

### Some constraints in such a system (II)

#### non-functional requirements

• Overall power consumption should be less than or equal to c

$$egin{aligned} P_{cpu} &= t_{cpu_{active}} * Vcc_{cpu} * I_{cpu_{active}} + (1 - t_{cpu_{active}}) * Vcc_{cpu} * I_{cpu_{inactive}} \ P_{system} &= P_{cpu} + P_{bus} + \dots \ P_{system} &\leq c \end{aligned}$$

• the battery should last at least d hours/days.

$$t_{life} = x \cdot V * y \cdot Ah/P_{system}$$
  
 $t_{life} \ge d$ 

Considered Systems

Example 00000000 Summary & Outlook

## Implementation details

Solver: ECL<sup>i</sup>PS<sup>e</sup> Prolog with IC library (integer and real interval arithmetic constraints)

#### Advantages:

- Easy combination of different constraint types (finite domain and interval arithmetic)
- Especially, real interval constraints make encoding of arithmetic constraints very straight forward.

Considered Systems

Example •0000000 Summary & Outlook

### Application to an example system - The Smart Vest

Could be used for fall detection and consists of the following components:

- 1 micro controller
- 1 bus
- 2 acceleration sensors
- 1 temperature sensor
- 1 Bluetooth module



Considered Systems

Example 0000000 Summary & Outlook

#### Program consists of 3 states

vest not worn:

Program flow

- acceleration sensors inactive
- temperature sensor active
- Bluetooth module inactive
- vest worn:
  - all sensors active
  - Bluetooth inactive
- fall detected:
  - Bluetooth active
  - all sensors inactive



Considered Systems

Example 00000000 Summary & Outlook

Setting of Components - Static parameters

MSP430: (3.3 V, f, (500 μA, 600 μA), (2.6 μA, 3 μA), {UART, SPI, I<sup>2</sup>C}, {UART, SPI})

Considered Systems

Example 00000000 Summary & Outlook

Setting of Components - Static parameters

- MSP430: (3.3 V, f, (500 μA, 600 μA), (2.6 μA, 3 μA), {UART, SPI, I<sup>2</sup>C}, {UART, SPI})
- 2 ADXL345: {(2.5 V, 6.25 Hz, 40 μA, 100 nA, {I<sup>2</sup>C, SPI}), (2.5 V, 1600 Hz, 100 μA, 100 nA, {SPI}), ...}

Considered Systems

Example 00000000 Summary & Outlook

Setting of Components - Static parameters

- MSP430: (3.3 V, f, (500 μA, 600 μA), (2.6 μA, 3 μA), {UART, SPI, I<sup>2</sup>C}, {UART, SPI})
- 2 ADXL345: {(2.5 V, 6.25 Hz, 40 μA, 100 nA, {I<sup>2</sup>C, SPI}), (2.5 V, 1600 Hz, 100 μA, 100 nA, {SPI}), ...}
- $I^2C$ -bus: {(...), (...)}
- 1 SHT21: {(..., {|<sup>2</sup>C})}
- 1 RN41: {(..., {UART})}

Considered Systems

Example 00000000 Summary & Outlook

Setting of Components - Static parameters

- MSP430: (3.3 V, f, (500 μA, 600 μA), (2.6 μA, 3 μA), {UART, SPI, I<sup>2</sup>C}, {UART, SPI})
- 2 ADXL345: {(2.5 V, 6.25 Hz, 40 μA, 100 nA, {I<sup>2</sup>C, SPI}), (2.5 V, 1600 Hz, 100 μA, 100 nA, {SPI}), ...}
- $I^2C$ -bus: {(...), (...)}
- 1 SHT21: {(..., {|<sup>2</sup>C})}
- 1 RN41: {(..., {UART})}
- 1 3.6 V 2 Ah battery

Considered Systems

Example 00000000 Summary & Outlook

# Setting of Components - Static parameters

- MSP430: (3.3 V, f, (500 μA, 600 μA), (2.6 μA, 3 μA), {UART, SPI, I<sup>2</sup>C}, {UART, SPI})
- 2 ADXL345: {(2.5 V, 6.25 Hz, 40 μA, 100 nA, {I<sup>2</sup>C, SPI}), (2.5 V, 1600 Hz, 100 μA, 100 nA, {SPI}), ...}
- $I^2C$ -bus: {(...), (...)}
- 1 SHT21: {(..., {|<sup>2</sup>C})}
- 1 RN41: {(..., {UART})}
- 1 3.6 V 2 Ah battery

Additional assumptions:

- f < 10 MHz (cpu limit)
- ADXL345 sensors sample rate ≥ 25 Hz (to make fall detection useful)

Considered Systems

Example 00000000 Summary & Outlook

## Setting of Components - Dynamic parameters

Dynamic parameters to estimate the later system behavior.

LOC the CPU needs for requesting and processing one sensor package:

Component	request LOC	process LOC
SHT21	1500	2000
ADXL345	2000	5000

Intended distribution of the system states:

State	Proportion	
Standby	75.00 %	
Fall detection	24.99 %	
Alarm	0.01 %	

Motivatio	o n
0000	

Considered Systems

Example 000000000 Summary & Outlook

### Constraints

 Modules can only be connected to interfaces, if they have at least one protocol in common.

Motivation	Considered Systems	Example	Summary & Outlook
0000	00000	00000000	00
Constraints			

 Modules can only be connected to interfaces, if they have at least one protocol in common.

 $\implies l^2C$  bus, as well as, SHT21 sensor should be connected to the first peripheral interface

 $\implies$  RN41 module has to be connected to the second interface

Motivation	Considered Systems	Example	Summary & Outlook
0000	00000	00000000	00
Constraints			

- Modules can only be connected to interfaces, if they have at least one protocol in common.
  - $\implies$   $I^2C$  bus, as well as, SHT21 sensor should be connected to the first peripheral interface
  - $\implies$  RN41 module has to be connected to the second interface
- All sensors on the same bus have to use the same protocol.

00000 00000 0000 00	
Motivation Considered Systems Example Summary	& Outle

 Modules can only be connected to interfaces, if they have at least one protocol in common.

 $\implies$   $I^2C$  bus, as well as, SHT21 sensor should be connected to the first peripheral interface

 $\Longrightarrow$  RN41 module has to be connected to the second interface

All sensors on the same bus have to use the same protocol.
 ADXL sensors have to use l<sup>2</sup>C protocol i.e. configurations only supporting the SPI protocol are not allowed.

nst

Motivation Co	nsidered Systems
0000 00	000

Example 000000000 Summary & Outlook

# Constraints

 Modules can only be connected to interfaces, if they have at least one protocol in common.

 $\implies l^2C$  bus, as well as, SHT21 sensor should be connected to the first peripheral interface

 $\implies$  RN41 module has to be connected to the second interface

- All sensors on the same bus have to use the same protocol.
  ADXL sensors have to use l<sup>2</sup>C protocol i.e. configurations only supporting the SPI protocol are not allowed.
- Cpu should be able to process the data of all connected sensors; State: vest worn (all three sensors active):

 $\frac{f_{adxl}^{1}7,000LOC + f_{adxl}^{2}*7,000LOC + f_{sht}*3,500LOC}{f_{cpu}}$ 

 $\implies f_{cpu} > 0.36 MHz$ 

Considered Systems

Example

Summary & Outlook

# Scenario (I)

### User knows:

nearly everything (has a clear idea about the system):

- $f_{cpu} = 2.4576 MHz$
- both acceleration sensors are clocked to 50*Hz*
- bus is clocked to 100*kHz*

### User does not know:

the expected system power consumption and/or system lifetime

Considered Systems

Example

Summary & Outlook

# Scenario (I)

### User knows:

nearly everything (has a clear idea about the system):

- $f_{cpu} = 2.4576 MHz$
- both acceleration sensors are clocked to 50*Hz*
- bus is clocked to 100*kHz*

### User does not know:

the expected system power consumption and/or system lifetime **Answer**:

power consumption: 9.455 mW ... 9.516 mW lifetime: around 31 days

Scenario (II)

Considered Systems

Example

Summary & Outlook

#### User does not know:

If it is possible to add an additional acceleration sensor (behaving like the others), but also to ensure that the system has an expected lifetime around 30 days.

Scenario (II)

Considered Systems

Example

Summary & Outlook

#### User does not know:

If it is possible to add an additional acceleration sensor (behaving like the others), but also to ensure that the system has an expected lifetime around 30 days.

#### Answer:

power consumption: nearly 10 mW sample rate: up to 200 Hz

Scenario (III)

Considered Systems

Example

Summary & Outlook

#### User does not know:

what are the highest possible sample rates for the 2 acceleration sensors while ensuring a system lifetime of about 2 weeks.

Scenario (III)

Considered Systems

Example

Summary & Outlook

#### User does not know:

what are the highest possible sample rates for the 2 acceleration sensors while ensuring a system lifetime of about 2 weeks.

#### Answer:

sample rates: 800 Hz and 400 Hz lifetime: around 20 days (nearly 3 weeks)

Considered Systems

Example 00000000 Summary & Outlook



- constraints are well suited to model conjunctions between components in an intuitive way, e.g. to ...
  - predict power consumption
  - predict system lifetime
  - check feasibility
- performance of the calculations are very promising
- system optimization

Considered Systems

Example 00000000 Summary & Outlook

### Future work

### Extend the area of application

- Prolog code in general is hard to read/develop/maintain, and
- current implementation is very special purpose

 $\implies$  Model-Driven-Development approach, where the user can specify his personal constraints for his concrete model, and the corresponding Prolog code is generated.

 Modelling the components through agents ⇒ running first simulations only using the design

Thank you!

Considered Systems

Example 00000000 Summary & Outlook

Please do not hesitate to ask any question!

- Benny Höckner
- Peter Sauer
- Thilo Vörtler
- Petra Hofstedt
- Thomas Hinze



E-Mail: benny.hoeckner@tu-cottbus.de